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				Application Number	Not Yet Known
				Filing Dat	November 28, 2000
				First Named Invent r	Stephen M. Trimberger
				Group Art Unit	Not Yet Known 2/32
				Examiner Name	Not Yet Known LEMMA
Sheet	1	of	2	Attorney Docket Number	X-805-8 US

[illegible][illegible]

Examiner Signature		Date Considered	10/08/2004
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Applicati n Number	Not Yet Known	
			Filing Dat	November 28, 2000	
			First Named Inv nt r	Stephen M. Trimberger	
			Group Art Unit	Not Yet Known 2132	
			Examiner Name	Not Yet Known LEMMA	
Sheet	2	of	2	Attorney Docket Number	X-805-8 US

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
S.L		Xilinx Application Note, "Configuration Issues: Power-Up, Volatility, Security, Battery Back-Up", XAPP 092, November 24, 1997 (Version 1.1).	
S.L		Virtex Configuration Guide, published by Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124, October 9, 2000 (Version 1.0).	
S.L		Bruce Schneier "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Second Edition, Copyright 1996, published by John Wiley & Sons, Inc.; Chapter 9, pages 193-194 and 200-203; Chapter 12, pages 265-301 and Chapter 15, pages 360-361	
S.L		Cahners EDN ACCESS Web Page, "Cunning circuits confound crooks," October 12, 2000; pages 1-8; http://www.ednmag.com/ednmag/reg/2000/10122000/21df2.htm	
S.L		Xilinx, Application Note, XAPP138, "Virtex FPGA Series Configuration and Readback", published October 4, 2000, available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124.	

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